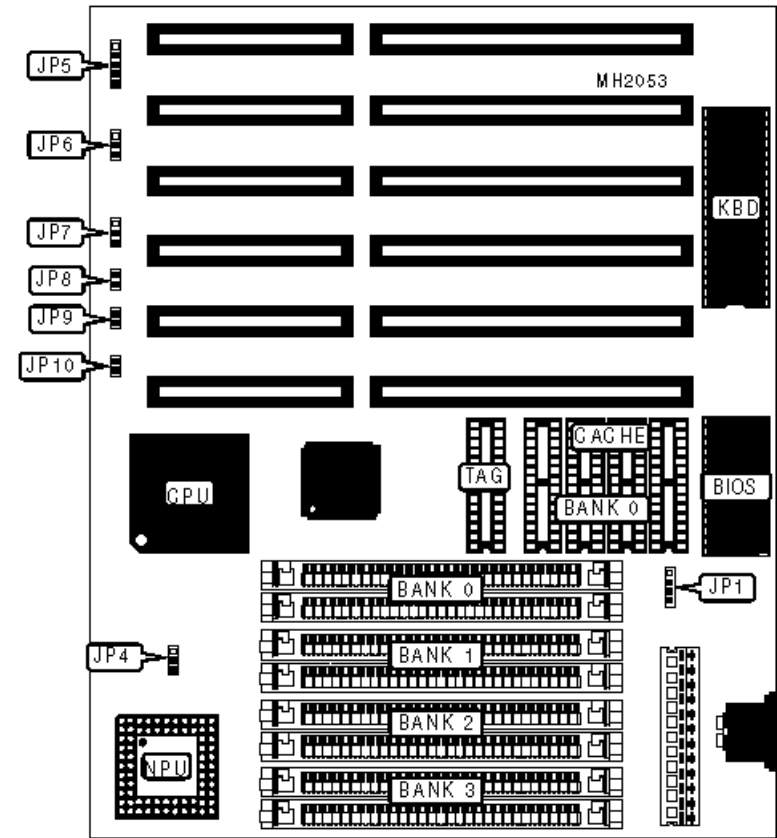


# UNIDENTIFIED

M-326

- Processor**            80386DX/CX486DLC
- Processor Speed**    33/40MHz
- Chip Set**             SARC
- Max. Onboard DRAM** 32MB
- Cache**                Unidentified
- BIOS**                 Unidentified
- Dimensions**         220mm x 170mm
- I/O Options**         None
- NPU Options**        80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED	JP8
Power LED & keylock connector	JP5	Turbo switch	JP9
Speaker connector	JP6	Reset connector	JP10

USER CONFIGURABLE SETTINGS

Function		Jumper	Position
»	CPU clock select 33MHz	JP4	pins 1 & 2 closed
	CPU clock select 40MHz	JP4	pins 2 & 3 closed
»	NPU disabled	JP7	pins 1 & 2 closed
	NPU enabled	JP7	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE
20MB	NONE	NONE	(2) 4M x 9	(2) 4M x 9
32MB	(2) 4M x 9	(2) 4M x 9	(2) 4M x 9	(2) 4M x 9

CACHE CONFIGURATION
Note: The exact configuration is unidentified by the manufacturer.