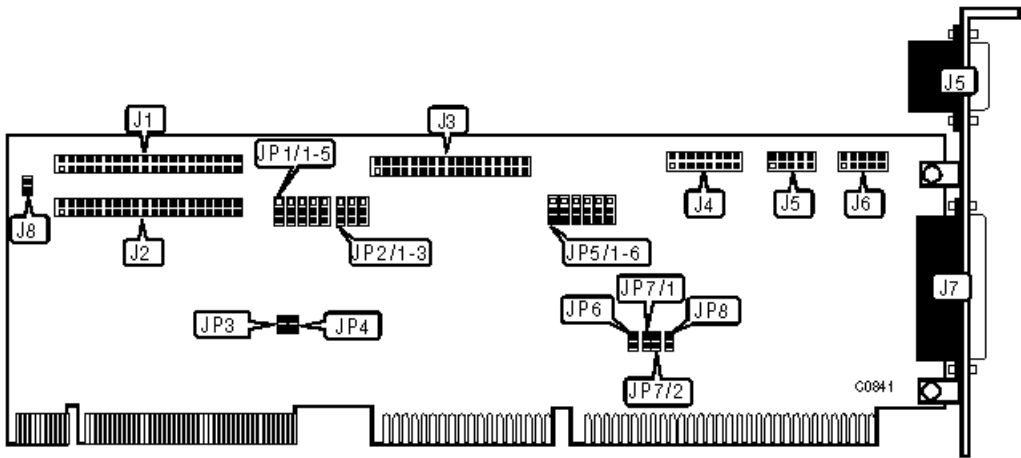


# DTK COMPUTER, INC.

PTI-255W (VER. 1.01)

<b>Data bus:</b>	32-bit VL-bus
<b>Size:</b>	Three-quarter length, half-height card
<b>Hard drive supported:</b>	Four IDE(AT) drives
<b>Floppy drives supported:</b>	Two 360KB, 720KB, 1.2MB, or 1.44MB drives



CONNECTIONS	
Function	Location
40-pin primary IDE(AT) connector	J1
40-pin secondary IDE(AT) connector	J2
34-pin cable connector - floppy drive	J3
16-pin game port	J4
10-pin serial port 1 - internal, 9-pin serial port 1 - external	J5
10-pin serial port 2 - internal	J6
25-pin Parallel port	J7
2-pin connector - drive active LED	J8

**DRIVE SPEED CONFIGURATION**

	<b>Speed</b>	<b>JP1/4</b>	<b>JP1/5</b>
»	600ns	pins 1 & 2 closed	pins 1 & 2 closed
	500ns	pins 2 & 3 closed	pins 1 & 2 closed
	400ns	pins 1 & 2 closed	pins 2 & 3 closed
	240ns	pins 2 & 3 closed	pins 2 & 3 closed

**USER CONFIGURABLE SETTINGS**

	<b>Function</b>	<b>Location</b>	<b>Setting</b>
»	Secondary IDE(AT) IOCHRDY signal disabled	JP3	open
	Secondary IDE(AT) IOCHRDY signal enabled	JP3	closed
»	Primary IDE(AT) IOCHRDY signal enabled	JP4	closed
	Primary IDE(AT) IOCHRDY signal disabled	JP4	open
»	Parallel port is unidirectional	JP6	closed
	Parallel port is bi-directional	JP6	open
»	Game port enabled	JP8	closed
	Game port disabled	JP8	open
»	IDE(AT) interface enabled	JP1/1	pins 1 & 2 closed
	IDE(AT) interface disabled	JP1/1	pins 2 & 3 closed
»	IDE(AT) address is 170h & 1F0h	JP1/2	pins 1 & 2 closed
	IDE(AT) address is 1F0h	JP1/2	pins 2 & 3 closed
»	CPU speed is 50MHz	JP1/3	pins 1 & 2 closed
	CPU speed is 33MHz	JP1/3	pins 2 & 3 closed
»	Floppy drive order is normal (A:, B:)	JP2/1	pins 1 & 2 closed
	Floppy drive order is reversed (B:, A:)	JP2/1	pins 2 & 3 closed
»	Floppy drive address is 3F0h	JP2/2	pins 1 & 2 closed

	Floppy drive address is 370h	JP2/2	pins 2 & 3 closed
»	Floppy drive interface enabled	JP2/3	pins 1 & 2 closed
	Floppy drive interface disabled	JP2/3	pins 2 & 3 closed

**SERIAL PORT 1 ADDRESS SELECTION**

	<b>COM</b>	<b>JP5/1</b>	<b>JP5/2</b>
»	COM1 (3F8h)	pins 1 & 2 closed	pins 1 & 2 closed
	COM3 (3E8h)	pins 2 & 3 closed	pins 2 & 3 closed
	COM4 (2E8h)	pins 1 & 2 closed	pins 2 & 3 closed
	Disabled	pins 2 & 3 closed	pins 1 & 2 closed

**SERIAL PORT 2 ADDRESS SELECTION**

	<b>COM</b>	<b>JP5/3</b>	<b>JP5/4</b>
»	COM2 (2F8h)	pins 1 & 2 closed	pins 1 & 2 closed
	COM3 (3E8h)	pins 1 & 2 closed	pins 2 & 3 closed
	COM4 (2E8h)	pins 2 & 3 closed	pins 2 & 3 closed
	Disabled	pins 2 & 3 closed	pins 1 & 2 closed

**PARALLEL PORT ADDRESS SELECTION**

	<b>LPT</b>	<b>JP5/5</b>	<b>JP6/6</b>
»	LPT1 (378h)	pins 1 & 2 closed	pins 1 & 2 closed
	LPT2 (278h)	pins 2 & 3 closed	pins 2 & 3 closed
	LPT3 (3BCh)	pins 2 & 3 closed	pins 1 & 2 closed
	Disabled	pins 1 & 2 closed	pins 2 & 3 closed

**PARALLEL PORT INTERRUPT SELECTION**

	<b>IRQ</b>	<b>JP7/1</b>	<b>JP7/1</b>
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»	7	closed	open
	5	open	closed