Declaration of conformity				
CE				
QUANTUM DESIGNS(HK) LTD. 5/F Somerset House, TaiKoo Place 979 Kings Road, Quarry Bay, Hong Kong				
	declare	s that the product		
Mainboard Advance 10B/Advance 10F				
is in conformity with (reference to the specification under which conformity is declared in accordance with 89/336 EEC-EMC Directive)				
 ✓ EN 55022 Limits and methods of measurements of radio disturban characteristics of information technology equipment ✓ EN 50081-1 Generic emission standard Part 1: Residential, commercial and light industry ✓ EN 50082-1 Generic immunity standard Part 1: Residential, commercial and light industry 		ethods of measurements of radio disturbance s of information technology equipment usion standard Part 1: commercial and light industry unity standard Part 1: commercial and light industry		
European Representative: QDI COMPUTER (UK) LTD QDI SYSTEM HANDEL GMBH QDI COMPUTER (FRANCE) SARL QDI COMPUTER (ESPANA) S.A. Signature :		QDI COMPUTER (SCANDINAVIA) A/S QDI COMPUTER (NETHERLANDS) B. V. QDI COMPUTER HANDELS GMBH QDI COMPUTER (SWEDEN) AB Place / Date : <u>HONG KONG/2000</u>		
Printed Name <u>:Lv Y</u>	′an	_ Position/ Title : <u>Assistant President</u>		

Declaration of conformity			
I			
Trade Name:	QDI Computer (U.S.A.) Inc.		
Model Name:	Advance 10B/Advance 10F		
Responsible Party:	QDI Computer (U.S.A.) Inc.		
Address:	41456 Christy Street		
	Fremont, CA 94538		
Telephone:	(510) 668-4933		
Facsimile:	(510) 668-4966		
Equipment Classification:	FCC Class B Subassembly		
Type of Product:	Mainboard		
Manufacturer:	Quantum Designs (HK) Inc.		
Address:	5/F, Somerset House, TaiKoo Place		
	979 Kings Road, Quarry Bay, HONG		
	KONG		
Supplementary Information:			
This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions : (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.			
Signature : Signature	C Date :2000		

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Advance 10B/Advance 10F

SpeedEasy Quick Setup

Procedures :

- 1. Correctly insert the processor you choose.
- 2. Plug in other configurations and restore the system.
- 3. Switch on power to the system and press the key to enter BIOS Setup.
- 4. Enter "SpeedEasy CPU SETUP" menu to set up the CPU speed.
- 5. Save and exit BIOS Setup, your system will now boot successfully.



SpeedEasy Introduction

SpeedEasy CPU Setup Menu

Select <SpeedEasy CPU SETUP> item from the main menu and enter the sub-menu:



Figure - 1 SpeedEasy CPU Setup Menu

BIOS provides you with a set of basic values for your processor selection instead of the jumper settings. The processor speed can be manually selected from the "SpeedEasy CPU SETUP" menu screen.

A Warning:

2

Do not set CPU frequency higher than its working frequency. If you do, we will not be responsible for any damages caused. Whether or not the system can be overclocked depends on the processor's capability. We do not guarantee the overclock system to be stable.

Users are provided with CPU overclock feature through "Jumper Emulation". The host bus speed can be set as 66/75/83/100/103/105/112/115/124/133/140/150MHz. The multiplier can be chosen from 3, 3.5, 4, 4.5, 5, 5.5, 6, 6.5, 7, 7.5, 8 or 7, 7.5, 8, 8.5, 9, 9.5, 10, 10.5, 11, 11.5, 12. However the multiplier setting will not function for bus ratio locked processor, only bus ratio unlocked processor.

Advance 10B/Advance 10F

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SpeedEasy ÖĐÑë (ÀíÆ÷Éè¶"2ä,¥

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1.7 Jumper Emulation, IAA,^{3,1}, @A%SIA*© CPU 予報上AU . 頂北※JUSUFERU 施河 66/75/83/100/103/105/112/115/124/133/140/150MHz. ±曙鏡近語記 3/3.5/4/ 4.5/5/5.5/6/6.5/7/7.5/8 » ∂7/7.5/8/8.5/9/9.5/10/10.5/11/11.5/12; 融州の近近距鏡 范囲 CPU, ご近記記記E , ±曙鏡短記後の冗話語書報題近本 CPU 極 .

Manual for Advance 10B/Advance 10F

Advance 10B/Advance 10F

Installation de la carte mère Advance 10B/Advance 10F

- 1. Assurez-vous que votre ensemble est complet: carte mère, câbles IDE et FLOPPY, notice d'utilisation et CD-ROM d'installation.
- Vérifiez que l'alimentation est débranchée et reliez-vous à la terre par une courroie à votre poignet. A défaut, maintenez le contact de vos deux mains avec un objet luimême relié à la terre, ou une partie en métal de votre système.
- 3. Fixez la carte mère dans le boîtier grâce aux vis fournies avec celui-ci.
- 4. Si votre carte mère est munie de cavaliers, placez les en fonction des options que vous souhaitez utiliser: réglage de la fréquence du processeur si votre carte n'est pas SpeedEasy, fonction allumage par saisie du mot de passe...(voir le manuel, rubrique «configuration des cavaliers»)
- 5. Insérez le processeur dans son logement avec son ventilateur que vous brancherez au connecteur «CPUFAN».
- 6. Insérez la/les barrette(s) mémoire dans les slots DIMM.
- Installez vos éventuelles cartes PCI et AMR dans les slots prévus à cet effet (voir page centrale du manuel).
- Branchez vos périphériques IDE et FLOPPY sur les connecteurs prévus à cet effet grâce aux nappes fournies avec la carte. Vérifiez que le sens de branchement est correct (liseré rouge du câble sur la broche 1 du connecteur).
- 9. Reliez les câbles du boîtier aux connecteurs prévus à cet effet (Connecteur d'alimentation, LED de marche/arrêt, disque dur, haut-parleur...). Refermez le boîtier.
- 10. Branchez les périphériques externes sur les sorties du fond de panier: clavier, souris PS/2, périphériques USB, moniteur, imprimante...
- 11. Lorsque tous les éléments du système sont installés physiquement, rebranchez l'unité centrale.

Installation du système.

- 1. Démarrez votre système en pressant le bouton «POWER».
- 2. Pressez la touche «Suppr» pour entrer dans le setup du BIOS.
- Dans le menu «SpeedEasy CPU Setup», réglez la vitesse de votre processeur (AT-TENTION: il est recommandé de ne pas sélectionner une fréquence supérieure à celle de votre processeur, nous déclinons toute responsabilité pour les dommages qui en résulteraient)
- 4. Effectuez les autres réglages du BIOS selon votre configuration (nous vous conseillons fortement de maintenir les réglages par défaut afin d'éviter toute manipulation hasardeuse pouvant résulter en un dysfonctionnement). Pour plus d'informations sur les fonctions du BIOS, vous pouvez consulter la version française du manuel sur le CD-ROM.
- 5. Pressez la touche F10 ou choisissez «Save and exit» pour enregistrer vos paramètres et relancer la machine.

Installation du Advance 10B/Advance 10F système

- 6. Installez votre système d'exploitation
- 7. Après installation, assurez-vous qu'il ne subsiste aucun conflit ou périphérique inconnu dans votre système.
- 8. Installation des pilotes:

1. Chipset:

Les pilotes des chipsets VIA du répertoire \ChipDrv\Via peuvent être utilisés sur cette carte mère.

Insérez le CD-ROM dans votre lecteur et cliquez sur « Chipset Driver » pour installer les pilotes

2. Logiciel PC-cillin 98:

Pour Windows 95/98, version anglaise, dans le répertoire \Pccillin\Win9X. Lancez setup.exe pour l'installation.

Pour Windows NT version anglaise, il se trouve dans le répertoire \Pccillin\Winnt40, lancez setup.exe.

Numéro de série: PNEF-9991-6558-5857-5535.

3. QDI ManageEasy:

Lancez le setup.exe du répertoire \QME pour installer le ManageEasy. Pour des informations détaillées sur le ManageEasy, référez-vous au manuel ManageEasy du répertoire \Doc.

N'oubliez pas de redémarrer votre système pour que les changements soient pris en compte.

4. RecoveryEasy

RecoveryEasy[™], la dernière innovation de QDI, permet de protéger le système des destructions en créant une «partition miroir» de la partition courante du disque dur et en sauvegardant toutes les données dans ce «miroir».

Cette utilitaire fournit partition du disque, récupération/sauvegarde des données, récupération/sauvegarde des réglages du CMOS et fonctions multi-boot.

RecoveryEasy permet également la protection du système contre les divers types de virus de boot tels que CIH. Dans le cas où le système est perdu soit par erreur, soit à cause d'un virus, il peut être récupéré depuis la partition miroir. Cette innovation utilise la technologie du Bios intégré qui n'occupe ni l'espace disque, ni la mémoire du système. C'est la solution idéale pour l'utilisateur.

Il faut presser les touches Ctrl + Bksp et F12 pour entrer dans les interfaces «Recovery» et «Partition» durant le démarrage du Bios.

ATTENTION : lisez attentivement le manuel du RecoveryEasy traduit sur le CD-ROM QDI avant d'installer cette fonction.

Très important : n'oubliez pas votre mot de passe, faute de quoi vous n'auriez plus accès au RecoveryEasy, même après avoir effectué un Clear CMOS.

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Advance 10B/Advance 10F

Le menu SpeedEasy

- I. Insérez le processeur correctement.
- II. Connectez les autres éléments du système (voir Installation).
- III. Au démarrage du système, pressez la touche <Suppr> pour entrer dans le Bios
- IV. Entrez dans le menu «CPU SpeedEasy setup»

Note: si vous ne déterminez pas la vitesse de votre unité centrale, votre système fonctionnera par défaut (200MHz pour les CPU avec une fréquence de Bus de 100MHz et 133MHz pour les CPU à 66MHz).

V. Sauvegardez et quittez le Bios.

CMOS Setup Utility – Copyright®1984-1999 Award Software

SpeedEasy CPU Setup

² U Model	: Intel® Celeron™	Item Help
^o U Speed	: 433 Mhz (66x6.5)	
Multiplier	X2	Menu Level >
Bus Clock	66MHz	
read Spectrum	Disabled	
2U Speed Multiplier Bus Clock pread Spectrum	: 433 Mhz (66x6.5) X2 66MHz Disabled	Menu Level >

Prévenir :

Le menu SpeedEasy vous fournit un ensemble de valeurs. Vous pouvez sélectionner manuellement la vitesse de CPU dans ce menu soit en mode «SpeedEasy» soit en mode «Jumper Emulation» (voir manuel).

ATTENTION: Ne pas sélectionner une fréquence de fonctionnement du CPU supérieure à celle indiquée par le constructeur. Nous déclinons toute responsabilité pour tout dégât qui en résulterait.

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8

Chapter 1 Introduction

Overview

The Advance 10B/Advance 10F green mainboard utilizes the Apollo Pro 133A chipset and provides high performance and cost-effective PC/ATX platform to support Intel Celeron[™] PPGA370, Pentium[®]III FC-PGA processors, Cyrix-III processor .The VIA[®]VT82C686B chipset provides new features with Ultra ATA/100. Equipped with three memory module sockets to support PC100/PC133MHz SDRAM and VCM SDRAM. It also provides advanced features such as Suspend to RAM, wake-up on LAN, wake-up on internal/external modem . The green function is in compliance with the ACPI specification.

IMPORTANT: take note of difference between two . The Advance 10B mainboard is that of the model with AC'97 codec, the Advance 10F mainboard is that of the model without AC'97 codec.

Key Features

Form factor

• ATX form factor of 305mm x210mm.

Microprocessor

- Supports all Intel[®]Pentium III(Coppermine) FC-PGA processors at 533/600EB/667/733/ 800EB/866/933MHz/1000MHz and future processors with 133MHz bus speed.
- Supports all Intel®Pentium III(Coppermine) FC-PGA processors at 500/550/600/650/ 700/750/800/850MHz and future processors with 100MHz bus speed.
- Supports all Intel[®]Celeron[™] FC-PGA processors at 533/566/600/633/667/700/733MHz and future processors with 66MHz bus speed.
- Supports all Intel[®]Celeron[™] PPGA processors at 300/300A/333/366/400/433/466/ 500/533MHz with 66MHz bus speed.
- Supports later processors Cyrix-III at 500/533/550/600/667MHz and future processors
- Supports 66/100/133MHz Front Side Bus.
- CPU core frequency = Bus speed x3, x3.5, x4, x4.5, x5, x5.5, x6, x6.5, x7, x7.5, x8 or x7, X7.5, X8, X8.5, X9, X9.5, X10, X10.5, X11, X11.5, X12. (BIOS will detect the CPU ratio)
- On-board 1.5V, 2.5V regulators .

Chipset

• Apollo Pro 133A chipset: VT82C694X, VT82C686B.

Introduction

System memory

- Provides three 168 pin 3.3V unbuffered 100/133MHz DIMM sockets.
- Supports both 100/133MHz SDRAM, VCM(Virtual Channel Memory) SDRAM.
- Minimum memory size is 8MB, maximum memory size is 1.5G MB.

On-board IDE

- Supports two PCI PIO and Bus Master IDE ports.
- Two fast IDE interfaces supporting four IDE devices including IDE hard disks and CD ROM drives.
- Supports up to mode 4 timing.
- Supports "Ultra DMA/33" Synchronous DMA mode transferring up to 33Mbytes/sec.
- Supports "Ultra DMA/66" Synchronous DMA mode transferring up to 66M bytes/sec.
- Supports "Ultra DMA/100" Synchronous DMA mode transferring up to 100 Mbytes/sec.
- Integrated 16x32bit buffer for IDE PCI Burst Transfers.

On-chip I/O

- One floppy port supporting up to two 3.5" or 5.25" floppy drives with 360K/720K/1.2M/1.44M/2.88M format.
- Two high speed 16550 fast compatible UARTs(COM1/COM2/COM3/COM4 selective) with 16-byte send/receive FIFOs.
- One enabled parallel port at the I/O address 378H/278H/3BCH with additional bi-direction I/O capability and multi-mode as SPP/EPP/ECP (IEEE 1284 compliant).
- Circuit protection provided, preventing damage to the parallel port when a connected printer is powered up or operates at a higher voltage.
- Supports LS-120 floppy disk drive and Zip drive.
- All I/O ports can be enabled/disabled in the BIOS setup.

On-chip Audio (Available for Advance 10B mainboard only)

- Build in VIA® 82C686B
- Direct Sound AC'97 Audio and AC'97 2.1 codec compliant.

AGP SLOT

• Supports 4X mode & AGP 2.0 compliant.

Advanced features

- PCI 2.2 Specification compliant.
- Provides Trend ChipAwayVirus® On Guard and PC-Cillin software with killing virus function.
- Provides four USB ports, onboard PS/2 mouse and PS/2 keyboard ports.
- Provides infrared interface.
- Support PC99 color- coding connector Specification



- Supports software power-down (e.g. Windows 98/Windwos 2000) .
- Supports wake-up on LAN and wake-up on internal/external modem.
- Supports auto fan off when the system enters suspend mode.
- supports system monitoring (monitors system temperature, CPU temperature, voltages and fan speed).
- Provides management application such as ManageEasy.
- Protects the system BIOS from being attacked by severe virus such as CIH, by enabling "Flash Write Protect" in CMOS setup or closing the Jumper "JAV".

BIOS

- Licensed advanced AWARD BIOS, supports flash ROM with 2M bit memory size, plug and play ready.
- Supports IDE CD-ROM or SCSI boot up.

Green function

- Supports ACPI (Advanced Configuration and Power Interface) and ODPM (OS Directed Power Management).
- Supports three green modes: Doze, Standby and Suspend.
- Supports ACPI power status: S0, S1, S3(STR), S4(STD), S5(Soft-off).

Expansion slots

- 1 ISA slots and 5 PCI slots.
- 1 AGP Slot.
- 1 AMR Slot.

Introduction

New Features

BIOS-ProtectEasy

The BIOS of the mainboard is contained inside the Flash ROM. Severe viruses such as CIH virus are so dangerous that it may overwrite the BIOS of the mainboard. If the BIOS has been damaged, the system will be unable to boot. We provide the following solution which protects the system BIOS from being attacked by such viruses.

There are two choices which implements this function.

- 1. Set the jumper (JAV) as closed, the BIOS can not be overwritten.
- Set the jumper (JAV) as opened, meanwhile set "Flash Write Protect" as Enabled in AWARD BIOS CMOS Setup. In this way, the BIOS can not be overwritten, but the DMI information can be updated.

Ultra ATA/100

According to the previous ATA/IDE hard drive data transfer protocol, the signaling way to send data was in synchronous strobe mode by using the rising edge of the strobe signal. The Ultra ATA/33 protocol doubles the burst transfer rate from 16.6MB/s to 33.3MB/s, by using both the rising and falling edges of the strobe signal, and Ultra ATA/66 doubles the Ultra ATA burst transfer rate once again (from 33.3MB/s to 66.6MB/s) by reducing setup times and increasing the strobe rate. In the same way, the burst transfer rate of Ultra ATA/100 is higher than ATA/66 (from 66.6MB/s to 100MB/s)by reducing the pulse width from 30ns to 20ns and increasing the strobe rate.

It can be added into existing systems without the need for termination devices, new cable, or other hardware changes, industry implementation of ATA/100 will provide an inexpensive, simple, non-proprietary, high-speed method of host to storage access.

The 80-conductor, 40-pin cable standard in ATA/66 has enhanced reliability and bee carried forward to ATA/100. of course, backwards compatible with ATA/33.

PC-133 Memory

PC133 SDRAM Unbuffered DIMM defines the electrical and mechanical requirements for 168-pin, 3.3 Volt, 133MHz, 64/72-bit wide, Unbuffered Synchronous DRAM Dual In-Line Memory Modules (SDRAM DIMMs). Relatively, the peak bandwidth of PC-133 memory is the 33% higher than PC-100 memory. These latest SDRAMs are necessary to meet the enhanced 133MHz bus speed requirement.

Suspend to RAM

Suspend to RAM is a cost-effective, optimal implementation of the Advanced Configuration and Power Interface (ACPI) 1.0 specification, which makes a PC's power consumption drop to the lowest possible level and enables quick wakeup. When the system is in Suspend-to-RAM status, the system context is maintained in system memory, the system consumes only a small fraction of the power used for full operation. Instead of shutting down the system to save power when not in use and then having to reboot later, Suspend-to-RAM solution enables the system to quickly wake up, restoring all applications and features, enabling operation in a few seconds.

To implement this function, the following requirments are essential:

- 1. Power supply requirements: The current of 5VSB line of the power supply should be more than 0.75A.
- 2. The BIOS option "ACPI function" should be enabled, and "ACPI Suspend Type" should be set as S3 in AWARD BIOS CMOS setup. Refer to page 35 for detail
- 3. An ACPI-enabled operating system such as Windows 98 or Windows 2000 family is needed.

a. In order to install ACPI-enabled Windows 98 operating system, the setup command should be typed as shown below:

D:\SETUP /P J

(This manual assumes that your CD-ROM device driver letter is D:).

- 4. Three ways to enter Suspend-to-RAM status under ACPI-enabled Windows 98:
 - Click Start -> Shut down -> Standby to enable the system to enter Suspend-to-RAM status.
 - Click Start -> Setup -> Control Panel -> Power Management -> Advanced and choose Standby item, the system will enter Suspend-to-RAM status when you press power button.
 - From Power Management Properties in Control Panel, set the latency time in System Standby, the system will enter Suspend-to-RAM status when time out.

The same ways used to power up the system can be used to wake up the system from Suspend-to-RAM status. For example, pushing the power button, through the Wake-on-LAN, Wake-on-Modem function or RTC Alarm.

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Chapter 2

Installation Instructions

This section covers External Connectors, Jumper Settings and Memory Configuration. Refer to the mainboard layout chart for locations of all jumpers, external connectors, slots and I/O ports. Furthermore, this section lists all necessary connector pin assignments for your reference. The particular state of the jumpers, connectors and ports are illustrated in the following figures. Before setting the jumpers or inserting these connectors, please pay attention to the directions.

Be sure to unplug the AC power supply before adding or removing expansion cards or other system peripherals, otherwise your mainboard and expansion cards might be seriously damaged.

External Connectors

PS/2 Keyboard Connector, PS/2 Mouse Connector

PS/2 keyboard connector is for the usage of PS/2 keyboard. If using a standard AT size keyboard, an adapter should be used to fit this connector. PS/2 mouse connector is for the usage of PS/2 mouse.



Two USB ports are available for connecting USB devices.



Two USB ports are not available on the back panel. Therefore, we provide a 10-pin ribbon cable with bracket to connect Built-in on-board USB header. (manufacturing option)



Manual for Advance 10B/Advance 10F

Installation Instruction

Parallel Port Connector and Serial Port Connector (UART1, UART2)

The parallel port connector can be connected to a parallel device such as a printer, while the serial port connector can be connected to a serial port device such as a serial port mouse. You can enable/disable them and choose the IRQ or I/O address in "Integrated Peripherals" from AWARD BIOS SETUP.



Line-in jack, Microphone-in jack, Speaker-out jack and MIDI/Joystick connector (option)

The Line-in jack can be connected to devices such as a cassette or minidisc player for playback or recording. The Microphone-in jack can be connected to a microphone for voice input. The Speaker-out jack allows you to connect speakers or headphones for audio output from the internal amplifier.

The MIDI/Joystick connector allows you to connect a game joystick or a MIDI device.



Note: The jacks and the MIDI/Joystick connector shown above are available for Advance 10B mainboard with AC'97 codec only.

ATX Power Supply Connector & Power Switch (POWER)

Be sure to connect the power supply plug to this connector in its proper orientation. The power switch (POWER) should be connected to a momentary switch (power button). When powering up your system, first turn on the mechanical switch of the power supply (if one is provided), then push once the power button. When powering off the system, you needn't turn off the mechanical switch, just *Push once*^{*} the power button.



Note: * If you change "soft-off by PWR-BTTN" from default "Instant-off" to "Delay 4 Secs" in the "POWER MANAGEMENT SETUP" section of the BIOS, the power button should be pressed for more than 4 seconds before the system powers down.

Manual for Advance 10B/Advance 10F



Hard Disk LED Connector (HD LED)

The connector connects to the case's IDE indicator LED indicating the activity status of IDE hard disk. The connector has an orientation. If one way doesn't work, try the other way.

Reset Switch (RESET)

The connector connects to the case's reset switch. Press the switch once, the system resets.

Speaker Connector (SPEAKER)

The connector can be connected to the speaker on the case.

Power LED Connector (PWRLED)

The power LED has four status. When the system is in power up status, the LED is on. When the system is in suspend status, the LED is blink. When the system is in Suspend to RAM, the LED is off. When the system is in Soft-Off status, the LED is off. The connector has an orientation.

ACPI LED Connector (ACPI_LED)(Reserved)

The ACPI LED is double-color lights with three pins. Pin1&Pin2 drive different color lights. If Pin1 drives orange light, Pin2 drives green light, the following status will come out. When the system is in power up status, the LED is green on. When the system is in suspend status, the LED is green blink. When the system is in suspend to RAM status, the LED is orange on. When the system is in soft-off status, the LED is off.

GREEN LED Connector (GREEN_LED)

The GREEN LED has four status. When the system is in three status (including power up, suspend, soft-off), the LED is off. When the system is in suspend to RAM status, the LED is on.

Hardware Green Connector (SLEEP)

Push once the switch connected to this header, the system enters suspend mode.



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Installation Instruction

Fan Connector (CPUFAN, CHSFAN)

The fan speed of these two fans can be detected and viewed in "PC Health" section of the BIOS. They will be automatically turned off after the system enters suspend mode.



Wake-Up On LAN (WOL)

Through the Wake-Up On LAN function, a wake event occurring from the network can wake up the system. If this function is to be used, please be sure an ATX 2.01 power supply of which 5VSB line is capable of delivering 720mA, and a LAN adapter which supports this function are used. Then connect this header to the relevant connector on the LAN adapter, set "Wake up on by Ring /LAN" as Enabled in the "POWER MANAGEMENT SETUP" section of the BIOS. Save & exit, then boot the operating system once to make sure this function takes effect.



Wake-Up On Internal Modem (WOM)

Through the Wake-Up On Internal Modem function, the system which is in the power-off status can be powered on by a ring signal received from the internal modem. If this function is to be used, be sure an internal modem card which supports the function is used. Then connect this header to the relevant connector on the modem card, set "Wake up On by Ring/LAN" as Enabled in the "POWER MANAGEMENT SETUP" section of the BIOS. Save & exit, then boot the operating system once to make sure this function takes effect.









Internal Audio Connectors (AUX, CDLIN, MODEM)

AUX and CDLIN connectors allow you to receive stereo audio input from such sound sources as a CD-ROM, TV tuner, or MPEG card. The MODEM connector allows the onboard audio to interface with a voice modem card with a similar connector. It also allows the sharing of mono_in (such as a phone) and mono_out (such as a speaker) between the onboard audio and the voice modem card.



Note: Internal Audio connectors shown above are available for Advance 10B mainboard with AC'97 codec only.

Audio/Modem Riser Interface Connector (AMR)

& AMR Sound Option(J4)

The AMR Interface Connector is the interface between the mainboard and the Audio/ Modem Riser card. The connector provides all necessary signals which supports several different configurations of audio and modem in the system, such as audio and modem on the Riser, audio on the mainboard and modem on the Riser, or no audio with modem on the Riser. Either AMR (Audio/Modem Riser) card or MR (Modem Riser) card can be used on this system. Avoild conflict, the onboard Audio can be disabled in BIOS when using AMR Audio Riser card.





Chassis Security Switch (CHSSEC)

The connector connects to the chassis security switch on the case. The system can detect the chassis intrusion through the status of this connector. If the connector has been closed once, the system will record the status and indicate the chassis has been opened. You can receive this information from QDI ManageEasy software.



Infrared Header (IrDA)

This connector supports wireless transmitting and receiving. If using this function, set "UART 2 Mode" to HPSIR or ASKIR and configure the settings from the "INTEGRATED PERIPHERALS" section of the BIOS.



Expansion Slots & I/O Ports description

Slot / Port	Description
PCI1	First PCI slot.
PCI2	Second PCI slot.
PCI3	Third PCI slot.
PCI4	Fourth PCI slot.
PCI5	Fifth PCI slot.
DIMM1	First DIMM slot.
DIMM2	Second DIMM slot.
DIMM3	Third DIMM slot.
IDE 1	Primary IDE port.
IDE2	Secondary IDE port.
AMR	AMR slot.
AGP	AGP slot.
FLOPPY	Floppy Drive Port.

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Jumper Settings

Jumpers are located on the mainboard, they represent, clear CMOS jumper JCC, BIOS-ProtectEasy jumper JAV. Pin 1 for all jumpers are located on the side with a thick white line (Pin1 \rightarrow ($\circ \circ \circ$), refer to the mainboard's silkscreen. Jumpers with three pins will be shown as $\bullet \circ \circ$ to represent pin1 & pin2 connected and $\circ \bullet \circ \circ$ to represent pin2 & pin3 connected.

FSB Frequency Selection(JFSB1, JFSB2)

The jumper labeled JFSB1 and JFSB2 are located on the mainboard providing users with FSB frequency selection. If it is set as Auto with JFSB1 and JFSB2 closed, the system detects the CPU front side bus automatically. If it is set as Jumper setting with JFSB1 and JFSB2 opened, the CPU front side bus can be set manually. For more FSB frequency selection, you could check it in AWARD BIOS CMOS Setup and set the proper frequency that you need.



Overclocking Jumper Setting (JCLK1, JCLK2)

Jumpers labeled JCLK1, JCLK2 are located on the mainboard providing users with CPU overclocking feature. The host bus speed can be set as 66/100/133MHz. Refer to the chart below for the location of these jumpers, and the table for information on how to set them.



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Installation Instruction

If CPU FSB is set as 66MHz, the system only run at 66MHz front side bus even if a processor with 100MHz or higher. If CPU FSB is set as 100MHz, the system can run at 100MHz front side bus even if a processor with 66MHz FSB is installed. Setting up to 133MHz FSB is also supported. However, whether or not your system can be overclocked depends on your processor's capability. Whether the processor is bus ratio locked or unlocked should also be taken into account. For bus ratio unlocked processor, this overclocking feature can be implemented by setting JFSB1 and JFSB2 as all opened, meanwhile respectively adjusting the bus clock higher and the bus ratio (Multiplier) lower in "SpeedEasy CPU Setup" in AWARD BIOS CMOS Setup. We do not guarantee the overclocking system to be stable.

BIOS-ProtectEasy Jumper (JAV)

The BIOS of the mainboard is contained inside the Flash ROM. If the jumper JAV is set as closed, you will be unable to flash the BIOS to the mainboard. However in this status, the system BIOS is protected from being attacked by serious virus such as CIH virus.



Setting the jumper JAV as opened(default), meanwhile disabling the "Flash Write Protect" item from "BIOS Features Setup" in AWARD BIOS CMOS Setup, allows you to flash the BIOS to the Flash ROM.

The DMI (Desktop Management Interface) system information such as the CPU type/speed, memory size, and expansion cards will be detected by the onboard BIOS and stored in the flash ROM. Whenever the system hardware configuration is changed, DMI information will be updated automatically. However, setting jumper JAV as closed makes flashing BIOS and updating DMI information impossible. Therefore, set JAV as closed when changing the system hardware configuration, or the error message "Unkown Flash Type" will be displayed on the screen, and DMI information update will be fail.

Setting the jumper JAV as closed, the Pentium[®] III Processor Number can be not readable whatever disabling or enabling "Processor Number Feature" item from "BIOS Features Setup" in AWARD BIOS CMOS Setup.

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Clear CMOS (JCC)

If you want to clear CMOS, unplug the AC power supply first, close JCC (pin1 & pin2) once, set JCC back to the normal status with pin2 & pin3 connected, then power on the system.



(Unplug the AC power supply)

Enable USB KeyDevice Wake-up Function (JUSB1, JUSB2)

The mainboard provides the advanced USB keyDevice wake-up function. The system can be waken up from its power saving status including ACPI S3 by activating USB keyDevice. When using this function, set JUSB1&JUSB2 with pin1 & pin2 closed. Otherwise, set JUSB1&JUSB2 with pin2 & pin3 closed for disabling.



AC97&MC97 Selection (JMC1, JMC2, JAC1)

Advance 10B/Advance 10F mainboard is compatible with AC97 and MC97 specification. Refer to the table for information on how to set them.



JAC1 JMC2		
	JMC1	
Jumper Function	JMC1	JMC2
AC97 Only	1-2	Close
AC97+MC97	1-2, 3-4	Open

'1-2' represents pin1 &pin2 closed. '3-4' represents pin3 & pin4 closed.

Note, For Advance 10F without AC'97 codec, the jumpers JAC1, JMC2, JMC1(1-2), JMC1(3-4) are shorted to support AMR device.

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Installation Instruction

Memory Configuration

This mainboard provides three 168 pin 3.3V PC133 un-buffered DIMM sockets to support a flexible memory size ranging from 8MB up to 1.5GB for SDRAM. PC100/PC133MHz SDRAM and later memory VCM (Virtual Channel Memory) SDRAM are supported, The following set of rules allows optimum configurations.

- Possible SDRAM DIMM memory sizes are 16MB, 32MB, 64MB, 128MB, 256MB, 512MB in each DIMM socket.
- Processor with 66MHz FSB should be paired with PC66, PC100, PC133 SDRAM processor with 100MHz FSB should be paired with either PC100 or PC133 SDRAM, processor with 133MHz FSB should be paired with PC133 SDRAM.
- supports Suspend to RAM.

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Chapter 3 BIOS Description

Utility Support:

AWDFLASH.EXE

This is a flash memory write/read utility used for the purpose of upgrading your BIOS when necessary. Before doing so, please note:

- We strongly recommend you only upgrade BIOS when encounter problems.
- Before upgrading your BIOS, review the description below to avoid making mistakes, destroying the BIOS and resulting in a non-working system.

When you encounter problems, for example, you find your system does not support the latest CPU released after our current mainboard, you may therefore upgrade the BIOS, please don't forget to set JAV as close and disable the "Flash Write Protect" item in AWARD BIOS CMOS Setup first.

Follow the steps exactly for a successful upgrade.

- 1. Create a bootable system floppy diskette by typing Format A:/s from the DOS prompt under DOS6.xx or Windows 9x environment.
- 2. Copy AWDFLASH.EXE from the directory \Utility located on QDI Mainboard Utility CD onto your new bootable diskette.
- 3. Download the updated BIOS file from the Website (http://www.qdigrp.com). Please be sure to download the suitable BIOS file for your mainboard.
- 4. Uncompress the file download, copy the BIOS file (xx.bin) onto the bootable diskette, and note the checksum of this BIOS which is located in readme file.
- 5. Reboot the system from the bootable diskette created.
- 6. Then run the AWDFLASH utility at the A:\prompt as shown below:

A:\AWDFLASH xxxx.bin

Follow the instruction through the process. Don't turn off power or reset the system until the BIOS upgrade has been completed.

If you require more detailed information concerning AWDFLASH Utility, for example, the different usage of parameters, please type A:\>AWDFLASH /?

Note: Because the BIOS Software will be updated constantly, the following BIOS screens and descriptions are for reference purposes only and may not reflect your BIOS screens exactly.

Manual for Advance 10B/Advance 10F

Award BIOS Description

AWARD BIOS Description

Entering Setup

Power on the computer, when the following message briefly appears at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press the <Ctrl> + <Alt> + <Esc> keys, to enter the AWARD BIOS CMOS Setup Utility.

Press to enter SETUP

Once you have entered, the Main Menu (Figure 1) appears on the screen. The main menu allows you to select from eleven setup functions and two exit choices. Use the arrow keys to select among the items and press the <Enter> key to accept or enter the submenu.



Figure-1 Main Menu

Load Fail-Safe Defaults

The Fail-Safe Defaults are secure and useful for system. It is recommended users load the Fail -Safe Defaults when the system is in trouble.

Load Optimized Defaults

The Optimized Defaults are common and efficient. It is recommended users load the optimized defaults first, then modify the needed configuration settings.

Standard CMOS Features Setup

The basic CMOS settings included in "Standard CMOS Features" are Date, Time, Hard Disk Drive Types, Floppy Disk Drive Types, and VGA etc. Use the arrow keys to highlight the item, then use the <PgUp> or <PgDn> keys to select the value desired in each item.

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Figure-2 Standard CMOS Setup Menu

For the items marked, press enter, a window will pop up as shown below. You can view detailed information or make modifications.

CMOS Setup Utility - Copyright (C) 1984-1999 Award Software IDE Primary Master			
IDE HDD Auto-Detection	n Press Enter	Item Help	
IDE Primary Master Access Mode Capacity Dylinder Head Precomp Landing Zone Sector	Auto Auto 3200 MB 11024 9 11023 53	Menu Level •• To auto-datect the HDD's size, head on this channe]	
fl==:Move Enter:Select F5:Previous Values	•/-/PU/PO;Value F18:Save F6:Fail-Safe Defaults F	ESC:Exit F1:General Help 7:Optimized Defaults	

Figure-2-1 IDE Primary Master Setup Menu

Hard Disk

Primary Master/Primary Slave/Secondary Master/Secondary Slave

These categories identify the HDD types of 2 IDE channels installed in the computer system. There are three choices provided for the Enhanced IDE BIOS: None, Auto, and User. 'None' means no HDD is installed or set; 'Auto' means the system can auto-detect the hard disk when booting up; by choosing 'user', the related information should be entered regarding the following items. Enter the information directly from the keyboard and press < Enter>:

CYLS	number of cylinders	HEAD	number of heads
PRECOMP	write pre-compensation	LANDZ	landing zone
SECTOR	number of sectors	MODE	HDD access mode

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Ы	Award BIOS Description

The Award BIOS supports 3 HDD modes: CHS, LBA and LARGE.

NORMAL

Generic access mode in which neither the BIOS nor the IDE controller will make any transformation during accessing. The maximum number of cylinders, heads and sectors for NORMAL mode are 1024,16 and 63.

If the user sets his HDD to CHS mode, the maximum accessible HDD size will be 528 megabytes even though its physical size may be greater than that.

LBA (Logical Block Addressing) mode

A new HDD accessing method to overcome the 528 Megabyte bottleneck. The number of cylinders, heads and sectors shown in setup may not be the number physically contained in the HDD.

During HDD accessing, the IDE controller will transform the logical address described by sector, head and cylinder number into its own physical address inside the HDD.

LARGE mode

Some IDE HDDs contain more than 1024 cylinder without LBA support (in some cases, users do not want LBA). The Award BIOS provides another alternative to support these kinds of HDD.

BIOS tricks DOS (or other OS) into divising the number of cylinders is less than 1024 by dividing it by 2. At the same time, the number of heads is multiplied by 2. A reverse transformation process will be made inside INT13h in order to access the right HDD address.

If using Auto detect, the BIOS will automatically detect the IDE hard disk mode and set it as one of the three modes.

Remark

To support LBA or LARGE mode of HDDs, there must be some softwares involved which are located in Award HDD Service Routine(INT13h). It may fail to access a HDD with LBA (LARGE) mode selected if you are running under an Operating System which replaces the whole INT 13h.



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Video

Set this field to the type of video display card installed in your system.

EGA/ VGA	Enhanced Graphics Adapter / Video Graphic Array. For EGA,
	VGA, SEGA, SVGA, or PGA monitor adapters.
CGA 40	Color Graphic Adapter, powering up in 40 column mode.
CGA 80	Color Graphic Adapter, powering up in 80 column mode.
MONO	Monochrome adapter, including high resolution monochrome adapters.

Halt On

This category determines whether or not the computer will stop if an error is detected during powering up.

No errors	The system boot will not stop for any errors that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not stop for a keyboard error; but it will stop for all other errors.
All, But Diskette	The system boot will not stop for a disk error; but it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error, but it will stop for all other errors.

Memory

This is a Display-Only Category, determined by POST (Power On Self Test) of the BIOS.

Base Memory	The POST of the BIOS will determine the amount of base
	(or conventional) memory installed in the system.
Extended Memory	The BIOS determines how much extended memory is
	presented during the POST.
Total Memory	Total memory of the system equals the sum of the above
	memory.

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SpeedEasy CPU Setup

Award BIOS Description

CMOS Setup Utility - Copyright IC1 1984-2000 Reard Software SpeedEarcy CPU SETUP			
CPU Nodel PENTRUMIRI III		Item Help	
Den Barlan Oft Hesting Clock Clock Ensty UDMUTCI (Spread Spectrum Warning: Be sure you is right, C will be den	r selection FU seer speed serves!	Menu Lovel •	
11**:Nove Enter:Select FS:Previous Values	*/*/PU/PD:Value F10:Save F6:Fail-Safe Defaults F	ESC:Exit F1:Ceneral Halp F7:Optimized Defaults	

Figure-3 SpeedEasy CPU Setup

The following indicates the options of each item and describes their meanings .

<u>ltem</u>	<u>Option</u>	Description
CPU Model	Intel(R)	BIOS can automatically detect the CPU model,
	Celeron(TM)	therefore this item is shown only.
CPU Speed	200MHz	CPU frequency should be set according to the CPU
	(66x3)	type.
		CPU frequency should be set according to the CPU
		type. For processors with 66MHz FSB you can
		choose from 200MHz (66X3) to 533MHz(66x8).
		For processors with 100MHz FSB, you can select
		from 300MHz(100X3) to 800MHz(100X8). For proces
		-sors with 133MHz FSB, you can select from 400MHz
		(133x3) to 1064MHz(133x8).
CPU Ratio	3~8	The multiplier can be chosen from 3~8 or 7~12. The
	7~12	BIOS will detect the CPU ratio and show the chooice
		range from 3 to 8 or 7 to 12. However the multiplier setting
		will not function for bus ratio locked processor, only bus
		ratio unlocked processor.
CPU Host/PCI	66/33MHz	Selects the CPU host bus clock and PCI clock.
Clock		
	150/37MHz	
 Close Empty 	Enabled	Closes empty DIMM or PCI clock to reduce EMI.
DIMM/PCI Clk	Disabled	Does not close empty DIMM or PCI clock.
 Spread 	0.25%	Enables Clock Spread Spectrum to reduce EMI.
Spectrum	0.5%	
	Disabled	Disables Clock Spread Spectrum to reduce EMI.

Warning:

Do not set CPU frequency higher than its working frequency. If you do, we will not be responsible for any damages caused.

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Advanced BIOS Features Setup

CMOS Setup Utility - Copyright (C) 1986-2000 Award Software Advanced BIOS Features			
Chip@wayWirus On Guard	Enabled	÷	Item Help
DPU Internal Cache External Cache DPU L2 Cache ECC Checking Processor Number Feature Quick Power On Self Test First Beat Device Bact Boot Device Boot Other Device Boot Other Device Boot Other Device Boot Other Device Boot Up Numlock Status Gate R20 Dotion Scient V Option OS Select For DRBM > 64MB HOD S.M.R.R. T. Capability Vides BIDS Shadow CEND-CEFFF Shadow	Enabled Enabled Enabled Enabled Enabled Floppy HOD-0 LS/ZIP En/ZIP Flop On Satled Orisabled Disabled Disabled Disabled		Menu Level • Allows you to choose the VIRUS merning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempt to write dute into this area, BIOS will shew a werning message on screen and alarm beep
11++:Hove Enter:Select +/-/ F5:Previous Values F6:	PU/PD:Value Fail-Safe D	F10:Save [efaults F	ESC:Exit F1:General Help 7:Optimized Defaults

Figure-4 BIOS Features Setup Menu

The following indicates the options for each item and describes their meaning.

ltem	Option	Description
 ChipAway Virus OnGuard 	Enabled	Guards against boot Virus threats early in the boot cycle, before they have a chance to load into your system, ensuring your computer boots to a clean operating system.
	Disabled	Invalidates this function.
• CPU	Enabled	Enables CPU internal Level1/Level2 cache.
Internal Cache	Disabled	Disables CPU internal Level1/Level2 cache.
External Cache	Enabled	Enables CPU external cache.
	Disabled	Disables CPU external cache.
CPU L2 Cache	Enabled	Enables CPU L2 Cache ECC function.
ECC Checking	Disabled	Disables CPU L2 Cache ECC function.
 Processor 	Enabled	Pentium [®] III Processor Number can be readable.
Number Feature	Disabled	Pentium [®] III Processor Number can be unreadable.
Quick Power On Self Test	Enabled	Enables quick POST. BIOS will shorten or skip some check items during POST to speed up POST after you power on the computer.
	Disabled	Normal POST.
• First (Second,	Disabled	Select Your Boot Device Priority. It could be
Third) Boot Device Boot other Device	Floppy	Disabled, Floppy, LS/ZIP, HDD-0, HDD-1, HDD-2, HDD-3, SCSI, CDROM, LAN.
 Swap Floppy 	Enabled	Exchanges the assignment of A&B floppy drives.
Drive	Disabled	The assignment of A&B floppy drives are normal.
 Boot Up 	On	Keypad is used as number keys.
Numlock Status	Off	Keypad is used as arrow keys.

Award BIOS Description

Gate A20	Normal	The A20 signal is controlled by the keyboard controller
Option		or chipset hardware.
	Fast	Default setting. The A20 signal is controlled by Port 92
		or the chipset specific method.
Security Option	System	Select whether the password is required every
	Setup	time the system boots or only when you enter
		setup.
 OS Select For 	Non-OS2	If your operating system is not OS/2, please select
DRAM>64MB		this item.
	OS2	If system DRAM is more than 64MB and the operating
		system is OS/2, please select this item.
• HDD S.M.A.R.T	Enabled	Enables S.M.A.R.T hard disk support.
Capability	Disabled	Invalidates this feature.
 Video BIOS 	Enabled	Video BIOS will be copied to RAM. Video Shadow
Shadow		will increase the video speed.
	Disabled	Video shadow is disabled.
• C8000~CBFFF	Enabled	Optional ROM will be copied to RAM by 16K bytes
Shadow:		per unit.
DC000-DFFFF		
Shadow:	Disabled	The shadow function is disabled.
 Delay For HDD 	0~3	Sets the pre-delay time for hard disk to be
0~3 (Secs):		accessed by the system.
 Show Bootup 	Disabled	Enables the logo when system boots up.
Logo	Enabled	Logo will not be shown when system boots up.
 Flash Write 	Enabled	Does not allow you to upgrade the BIOS.
Protect		Note: Enabling this item can protect the
		system BIOS from being attacked by severe
		virus such as CIH. Therefore disable this
		item item only when wanting to flash BIOS,
		afterwards set this item as Enabled (default).
	Disabled	Disabling this item allows you to upgrade the BIOS.

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Advanced Chipset Features Setup

CMOS Setup Utility	y - Copyright (C) 19 Advanced Chipset Fe	84-2000 F atures	ward Software
Bank 0/1 DR9M Tiwing	SDRIM 10ns	A	Item Help
bank 2/3 USAH Timing Bank 4/5 USAH Timing SRHM Cycle Length USAH Cycle Length DGRM Clock Mamory Hale P2C/22P Concurrency Fast R-V Iurn Around System EUIS Enchashle Video NHM Cancheable Video NHM Cancheable Video NHM Cancheable RAP Driving Control RAP Driving Control RAP Driving Control RAP Driving Control RAP Driving Control Bang Control Support DeChip Sound DeChip Nodem	SUBOM 19ms SUBOM 19ms Bost CLK Disabled Enabled Disabled SAM Enabled Auto Enabled Enabled Enabled Disabled Enabled Probled Bisabled Auto		Henu Lovel 🕞
14=+:Move Enter:Select	+/-/PU/PD:Value F1 E6:Eail-Safa Dafas	0:Save E	SC:Exit F1:General Help Continized Defaults

Figure-5 Advanced Chipset Features Setup Menu

The following indicates the options for each item and describes their meaning.

ltem	<u>Option</u>	Description
• Bank 0/1, 2/3, 4/5	SDRAM 8/10ns	These items are of selected DRAM read/write
DRAM Timing	Normal	timing. Default setting is recommended.
	Medium	
	Fast	
	Turbo	
 SDRAM Cycle 	2/3	Define the CLT timing parameter of SDRAM
Length	Auto	expressed in 66MHz clocks.
		Latency Time = 2 clocks
		Latency Time = 3 clocks
DRAM Clock	Host Clk	DRAM frequency same as CPU FSB
	Hclk-33M	DRAM frequency is faster than CPU FSB by 33MHz.
Memory Hole	Enabled	Memory Hole at 15-16M is reserved for expanded
		ISA card.
	Disabled	Do not set this memory hole.
 P2C/C2P 	Enabled	Enabled P2C/C2P concurrency
Concurrency	Disabled	Disable P2C/C2P concurrency
 System BIOS 	Enabled	Beside conventioal memory, system BIOS area is
Cacheable		also cacheable.
	Disabled	System BIOS area is not cacheable.
 Video RAM 	Enabled	Besides conventional memory, video RAM is also
Cacheable		also cacheable.
	Disabled	Video RAM area is not cacheable.

Award BIOS Description

AGP Aperture Size	4~128	Sets the effective size of the Graphics Aperture	
(MB)		to be used in the particular PAC Configuration.	
 AGP-4X Mode 	Enabled	Supports 4X mode.	
	Disabled	Does not support 4X mode.	
 AGP Driving Control 	Auto	The default setting is suggested.	
	manual		
 AGP Driving Value 	EC	Sets the AGP Driving Value when the 4X AGP	
		card runs incorrectly.	
 AGP Fast Write 	Enabled	Enable AGP Fast Write	
	Disabled	Disable AGP Fast Write	
 Onchip USB 	Enabled	Enables the onchip USB controller.	
	Disabled	Disables the onchip USB controller.	
 USB Keyboard 	Enabled	USB keyboard support is enabled.	
Support	Disabled	USB keyboard support is disabled.	
 Onchip Sound 	Auto	Enable AC97 function.	
	Disabled	Disable AC97 function.	
		Note: If you have conflicts with the onboard	
		audio, you may set to disable this function.	
		The item is available for Advance 10B mainboard	
		only.	
Onchip Modem	Auto	Enable MC97 function.	
Onchip Modem	Auto Disabled	Enable MC97 function. Disable MC97 function.	
Onchip Modem CPU to PCI Write	Auto Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer.	
 Onchip Modem CPU to PCI Write Buffer 	Auto Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci 	Auto Disabled Enabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting 	Auto Disabled Enabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS 	Auto Disabled Enabled Disabled Enabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write 	Auto Disabled Enabled Disabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master0 WS Write.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay 	Auto Disabled Enabled Disabled Disabled Enabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master0 WS Write. Enable PCI Delay Transaction.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction 	Auto Disabled Enabled Disabled Disabled Enabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 	Auto Disabled Enabled Disabled Disabled Disabled Enabled Disabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI#2 Access #1 Retry.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry 	Auto Disabled Enabled Disabled Disabled Enabled Disabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS 	Auto Disabled Enabled Disabled Disabled Enabled Disabled Disabled Enabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS Write 	Auto Disabled Enabled Disabled Disabled Enabled Disabled Disabled Enabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS Write AGP Master 1 WS 	Auto Disabled Enabled Disabled Disabled Disabled Disabled Disabled Disabled Enabled Disabled Enabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write. Enable AGP Master 1 WS Read.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS Write AGP Master 1 WS Read 	Auto Disabled Enabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write. Enable AGP Master 1 WS Read. Disabled AGP Master 1 WS Read.	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS Write AGP Master 1 WS Read Memory Parity/ECC 	Auto Disabled Enabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Enabled Disabled Enabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI Delay Transaction. Enable PCI #2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write. Enable AGP Master 1 WS Write. Enable AGP Master 1 WS Read. Disabled AGP Master 1 WS Read. Enables the Error Checking&Correction if ECC	
 Onchip Modem CPU to PCI Write Buffer PCI Dynamci Bursting PCI Master 0 WS Write PCI Delay Transaction PCI#2 Access #1 Retry AGP Master 1 WS Write AGP Master 1 WS Read Memory Parity/ECC Check 	Auto Disabled Enabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Disabled Enabled Disabled Enabled	Enable MC97 function. Disable MC97 function. Enable CPU to PCI Write Buffer. Disable CPU to PCI Write Buffer. Enable PCI Dynamci Bursting. Disable PCI Dynamci Bursting. Enable PCI Master 0 WS Write. Disable PCI Master 0 WS Write. Enable PCI Master 0 WS Write. Enable PCI Delay Transaction. Disable PCI Delay Transaction. Enable PCI#2 Access #1 Retry. Disable PCI#2 Access #1 Retry. Enable AGP Master 1 WS Write. Disabled AGP Master 1 WS Write. Enable AGP Master 1 WS Read. Disabled AGP Master 1 WS Read. Enables the Error Checking&Correction if ECC memory is used.	

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Power Management Setup

CHOS Setup Utility - Copyright (C) 1986-2000 Award Software Power Management Sotup				
ACPI function	Enabled	Item Help		
- Ford Annoplant HFT Suspend Type FM Centrol by HM Video Off Option Video Off Option WEGM Use INO Soft-Off by FARBIN Soft-Off by FARBIN State Hiter Power Failt	Frension Stife Sussend -> Off V/II SWC-Blank 3 Instant-Off Press Enter	Henu Lovel •		
11**:Move Enter:Select - F5:Previous Values	/-/PU/PD:Value F10:Save i F6:Fail-Safe Defaults F.	ESC:Exit F1:General Help 7:Optimized Defaults		

Figure-6 Power Management Setup Menu

The following indicates the options for each item and describes their meaning.

ltem	<u>Option</u>	Description
 ACPI function 	Enabled	Validates ACPI function.
	Disabled	Invalidates ACPI function.
Power	User Define	Users can configure their own Power Management
Management		Timer.
	Min Saving	Pre - defined timer values are used. All timers are in their MAX values.
	Max Saving	Pre - defined timer values are used. All timers are in
		their MIN values.
 ACPI Suspend 	S1	Selects the ACPI suspend type.
Туре	S3	
 PM Control by 	NO	System BIOS will ignore APM when Power
APM		Management is enabled.
	Yes	System BIOS will wait for APM's prompt before
		entering any PM mode e.g. Standby or Suspend.
 Video Off option 	All Modes-off	Screen blanks when enters all modes.
	Suspend-off	Screen blanks after the system enters suspend mode.
	Always on	screen is always on.
 Video Off Method 	Blank Screen	The system BIOS will only blank off the screen when disabling video.
	V/HSYNC+	In addition to Blank Screen, BIOS will also turn
	Blank	off the V-SYNC & H - SYNC signals from VGA
		cards to monitor.
	DPMS	This function is enabled only for the VGA card supporting DPMS.

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Award BIOS Description

 Soft-off by PWRBTN 	Instant-off	The system will power off immediately once the power button is pressed.	
	Delay 4 Sec	The system will not power off until the power	
		button has been pressed continuously for more	
		than 4 seconds.	
 Modem Use IRQ 	3,5,7,9,10,11 NA	Special Wake-up event for Modem.	
 Wake Up Events 	Press Enter	set the following items.	
USB Resume from S3	Enabled	When system enters S3 state, USB can be waken up.	
	Disabled		
• VGA	on	VGA active reloads global timer.	
	Off	VGA active has no influence to global timer.	
LPT&COM	LPT/COM	Set the options of these items to reload global	
HDD&FDD DCI/master	Off/On	timer.	
PCI/master	on/off		
 PowerOn by PCI 	Enable	Allows any event occurring to the DMA controller	
Card		will awaken a system which has been powered	
		down.	
	Disable	Disables power-on by PCI card.	
 Wake up On by 	Enabled	Allows the system to be powered on when a ring	
Ring/ LAN		indicator signal comes up to UART1 or UART2	
		from an external modem or comes up to WOM	
		header from an internal modem card, or when a	
		remote wake up signal comes up to the WOL	
		header from LAN adapter.	
	Disabled	Does not allow wake up on LAN or wake up	
		from internal/external modem.	
RTC Alarm	Enabled	RTC alarm can be used to generate a wake event	
Resume		to power up the system which is in power-off	
		status. You can set any date or any time to	
		power up the system.	
	Disabled	RTC has no alarm function.	
 Primary INTR 	On	Allows wake up from IRQ.	
	Off	Does not Allows wake up from IRQ.	
 IRQs Activity 	Press Enter	Reloads global timer.	
Monitoring			

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PNP/PCI Configuration Setup

CMOS Setup Utility - Copyright (C) 1986-2000 Award Software PnP/PCI Configurations			
PMP 05 Installed Reset Configuration Data Resources Controlled By IRG Resources × DMH Resources PCL/VGH Palette Secop Assign IRO For VGA Resign IRO For USS	fool Disebled Auto(ESCD) Press Enter Press Enter Disebled Enabled Enabled	Item Help Menu Level • Selact Ves if you are using a Plug and Play camble operating system Selact We if you need the BIDS to configure non-boot devices	
11:Nove Enter:Select */-/	PW/PD:Value F18:Save F	SC:Exit F1:General Held	

Figure-7 PNP/PCI Configuration Setup Menu

The following indicates the options for each item and describes their meaning.

Item	Option	Description
 PNP OS Installed 	Yes	Device resources assigned by PnP OS.
	No	Device resources assigned by BIOS.
Reset Configuration	Enabled	The system BIOS will reset configuration data
Data		once then automatically set this item as disabled.
	Disabled	Disables the configuration data function.
 Resources 	Manual	Assigns the system resources (IRQ and DMA)
Controlled By		manually.
	Auto	Assigns system resources (IRQ and DMA) auto-
		matically by BIOS.
 PCI/VGA Palette 	Enabled	Enabled PCI/VGA Palette Snoop.
Snoop	Disabled	Disabled PCI/VGA Palette Snoop.
 Assign IRQ For 	Enabled	Assigns the needed IRQ for the VGA card.
VGA	Disabled	Does not assign an IRQ for the VGA card, in
		order to release the IRQ.
 Assign IRQ For 	Enabled	Assigns an IRQ for USB. If an USB device is used
USB		enables this item.
	Disabled	Does not assign an IRQ for USB.
• IRQ-3~IRQ-15	Legacy ISA	The specified IRQ-x will be assigned to ISA only.
assigned to	PCI/ISA PnP	The specified IRQ-x will be assigned to ISA or PCI.
 DMA-0~DMA-7 	Legacy ISA	The specified DMA-x will be assigned to ISA only.
assigned to	PCI/ISA PnP	The specified DMA-x will be assigned to ISA or PCI.

Award BIOS Description

Integrated Peripherals

CMOS Setup Utility	- Copyright (C) Integrated Per	1984-2000 F ipherals	ward Software	
OnChip IDE Channel@	Enabled	A I	Item	Help
URCHIP LUC Character Primary Master PTD Secondary Master PTD Secondary Master PTD Primary Kaster UDM Primary Kaster UDM Primary Slava UDM Secondary Master UDM Secondary Slava UDM Int Dabley Sirvest LUC Dables UDM LUC Dables A UDM Dables * TX.FXX inverting enable	Endoled Endoled Auto Auto Auto Auto Auto Auto Auto Endoled Endoled Standard Kilo Standard Kilo Ves		Menu Lovel	•
14++:Hove Enter:Select	+/-/PU/PD:Value	F18:Save E	SC:Exit F1:G	eneral Help

Figure-8 Integrated Peripherals Menu

The following indicates the options for each item and describes their meaning.

ltem	<u>Option</u>	Description
OnChip IDE	Enabled	Enables OnChip IDE First/Second Channel.
channel 0/1	Disabled	Disables OnChip IDE First/Second Channel.
IDE Prefetch/Mode	Enabled	Enables IDE Prefetch Mode.
	Disabled	Disables IDE Prefetch Mode.
• IDE	Mode 0 - 4	Defines the IDE primary/secondary master/ slave
Primary/ Secondary		PIO mode.
Master/Slave PIO	Auto	The IDE PIO mode is defined by auto -detection.
• IDE	Auto	Ultra DMA mode will be enabled if an ultra DMA
Primary/ Secondary		device is detected.
Master/Slave UDMA	Disabled	Disables this function.
 Init Display First 	PCI SLOT	Initializes the PCI VGA first. If a PCI VGA card
		and an AGP card are installed together in the
		system, the one initialized first functions.
	AGP	Initializes the AGP first.
 IDE HDD Block 	Enabled	Allows IDE HDD to read/write several sectors
Mode		at once.
 Onboard FDC 	Enabled	Onboard floppy disk controller is enabled.
Controller	Disabled	Onboard floppy disk controller is disabled.
 Onboard Serial 	3F8/IRQ4,	Defines the onboard serial port address and required
Port 1/2	2F8/IRQ3,	interrupt number.
	3E8/IRQ4,	
	2E8/IRQ3,	
	Auto	Onboard serial port address and IRQ are auto- matically assigned
	Disabled	Onboard serial port is disabled.

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UART 2 Mode	Standard HPSIR ASK IR	Defines Serial Port 2 as standard serial port. Supports IRD mode. Supports SHARP ASK-IR protocol with maximum baud rate up to 57600bps.
Onboard Parallel	378/IRQ7.	Defines onboard parallel port address and IRQ
Port	278/IRQ5,	channel.
	3BC/IRQ7	
	Disabled	Onboard parallel port is disabled.
Parallel Port Mode	SPP	Defines the parallel port mode as
	EPP	Standard Parallel Port (SPP), Enhanced
	ECP,	Parallel Port (EPP), or Extended
	ECP+EPP	Capabilities Port (ECP).
ECP Mode Use	1, 3	Set DMA for ECP mode use.
DMA		
Parallel Port EPP Type	EPP1.7 EPP1.9	Set parallel port EPP type.

The following items are available for Advance 10B mainboard only.

Onboard Legacy Audio	Enabled	the following item according as onboard audio to set
	Disabled	
 Sound Blaster 	Enabled	Enabled Sound Blaster.
	Disabled	Disabled Sound Blaster.
 SB I/O Base 	220H/240H	Define SB I/O Base Address.
Address	260H/280H	
 SB IRQ Select 	IRQ5~IRQ10	Select SB IRQ.
SB DMA Select	DMA0~DMA3	Select SB DMA .
 MPU-401 	Enabled	Enable MPU-401
	Disabled	Disable MPU-401
 MPU-401 I/O 	310/313H~	Define MPU-401 I/O address.
Address	320-323H	
 Game port 	Enabled	Enable game port.
(200-207H)	Disabled	Disable game port.
(200-207H)	Disabled	Disable game port.

Award BIOS Description

PC Health Status

CMOS Setup Utility - Copyright (C) 1984-2000 Award Seftware PC Health Status			
Current CPU Temp.	: 39°C/102°F	Item Help	
Larrent System Tempe Darrent CHERN Speed Darrent CHSTNN Speed Vone 2.59 3.69 3.59 1.29	30/ Cloby* 3028 RFM 3008 RFM 1.5V 2.49V 3.32V 4.83V 11.79V	Henu Level →	
18++:Hove Enter:Select E5:Provious Values	+/-/PU/PD:Value F18:Save E6:Enil-Safe Defaults	ESC:Exit F1:General Help E7:Optimized Defaults	

Figure-9 PC Health Status Menu

The following describes the meaning of each item.

<u>ltem</u>	Current <u>Data Shown</u>	Description
 Current CPU Temp Current System Temp. Current CPUFAN Speed Current CHSFAN Speed 	39°C/102°C 30°C/ 86°F 3999RPM 3998RPM	Temperature of the CPU core. Temperature inside the chassis. RPM(Revolution Per Minute) speed of fan connected to the fan header CPUFAN/ CHSFAN. Fan speed value is based on an assumption that tachometer signal is two pulses per revolution; In other cases, you should regard it relatively.
 Vcore 2.5V 3.3V 5V 12V 	1.5V 2.49 3.32V 4.83V 11.79V	Displays current Voltage values including all significant voltages of the mainboard. +3.3V, +2.5V, +12V and 5V are voltages from the ATX power supply, Vcore Voltage is the CPU core voltage from the on board switching power supply.

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Supervisor/ User Password

When this function is selected, the following message appears at the center of the screen to assist you in creating a password.

ENTER PASSWORD

Type the password, up to eight characters, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter BIOS Setup freely.

PASSWORD DISABLED

If you have selected "**System**" in "Security Option" of "BIOS Features Setup" menu, you will be prompted for the password every time the system reboots or any time you try to enter BIOS Setup.

If you have selected "**Setup**" at "Security Option" from "BIOS Features Setup" menu, you will be prompted for the password only when you enter BIOS Setup.

Supervisor Password has higher priority than User Password. You can use Supervisor Password when booting the system or entering "CMOS Setup" to modify all settings. Also you can use User Password when booting the system or entering "CMOS Setup" but can not modify any setting if Supervisor Password is enabled.

Boot with BIOS defaults

If you have made all the changes to CMOS values and the system can not boot with the CMOS values selected in setup, clear CMOS after power-down, then power on again. System will boot with BIOS default settings.

Award BIOS Description

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Appendix A QDI Driver CD 2000

A QDI Driver CD 2000 is supplied with this mainboard. Insert CD 2000 that came with your mainboard into your CD-ROM drive to bring up the screen, click the options to install. The contents contained in it are showed as below:

1. Express Install

It's recommended for most users that program will be installed with the most common options.

A. Chipset Driver B. Audio Codec Driver

A. Chipset Driver

It's recommended for advanced users that you may choose the options you want to install chipset driver

ViA Chipset Drivers included in the directory \ChipDrv\Via .

B. Audio Codec Driver

It's recommended for advanced users that you may choose the options you want to install Audio driver

For Windows 98, click this option you can install the drivers for your Onboard sound. For Windows 2000, system will detect the sound device and install the drivers automatically.

4. Accessory

The softwares contained in this directory are:

- A. DirectX 7.0 B. QDI ManageEasy
- C. PC-cillin

5. Browse CD

You could read all the contents contained in this CD, including Utility and Documents.

В.

Lf.exe

The files included in Utility are:

A. Awdflash.exe

The files included in **Documents** are:

- A. Adobe Acrobat Reader V3.0 Ar32e301.exe
- B. French Manual AD5 FR.doc, ect.

In windows 2000, The contents contained in CD are showed as below:

- 1. Audio Codec Driver
- 2. QDI ManageEasy
- 3. Browse CD

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PC-cillin 98

New viruses are appearing frequently; the chance of your PC being infected increases; antivirus softwares are becoming a must. PC-cillin 98 offers you full-time active virus protection as well as manual scans, plus virus clean capability. Keeping up to date on the latest threats and updating significant files are crucial in keeping antivirus software effective. PC-cillin 98 provides Free Virus Pattern File Updates from the Trend Micro Website:

http://www.trend.com/download/pattern.htm or http://www.antivirus.com/download/pattern.htm.

Installation of PC-cillin 98

For Windows 95/98 English version, run Setup.exe for installation from the utility CD directory \Pccillin\Win9x.

For Windows 95/98 Chinese version, run Setup.exe for installation from the utility CD directory \Pccillin\PWin9x.

For Windows NT 4.0, run Setup.exe for installation from the utility CD directory \Pccillin\WinNT4.0.

S/N is PN EF-9991-6558-5857-5535.

QDI ManageEasy V2.0

It is well known that guaranteeing the computer's security and reliability is essential. Especially today, effectively managing and monitoring the computer's hardware is even more important; because processing and exchanging critical data through computer and network are happening everyday.

Moving with the computer's development, the system of the computer will become more and more complex; at the same time, the control computer's hardware will be strengthened. Today, it is possible to monitor and manage your complex hardware from Windows 9X and Windows NT. QDI ManageEasy is a system tool, a bridge between the complex hardware and OS, used to access hardware status and to execute control functions. It supports stronger functions for Windows 9X and Windows NT. These functions enables you to view more than one hundred of the basic information about the system and monitor some key reference data concerning computer health in real time. QDI ManageEasy also helps you to use remote access and control computers in

your local area network. With QDI ManageEasy, you can improve your management level.

Installation of QDI ManageEasy V2.0

Run Setup.exe from the utility CD directory \QME2 to install the QDI ManageEasy V2.0. The QDI ManageEasy Setup Wizard will guide you through the installation process. For detailed information on how to use QDI ManageEasy V2.0, please refer to the QDI ManageEasy V2.0 online help.

Appendix

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Appendix B. Boot Logo

When you power on or reset your system, the picture shown below will be displayed on the screen.



You can use "CBLOGO.EXE" (included on the QDI Mainboard Utility CD) to replace it by any other logo which you prefer.

Please you follow these steps to use CBLOGO.EXE Utility:

- 1. Copy "CBLOGO.EXE" and "AWDFLASH.EXE" from the directory \Utility located on QDI Mainboard Utility CD onto your hard disk.
- 2. Get the BIOS file from "AWDFLASH.EXE" or Download the BIOS file from the Website (http://www.qdigrp.com). and copy the BIOS file(xxxxxx.bin) onto your hard disk.
- 3. Boot the system into DOS environment, Put your favor picture into BIOS file by "CBLOGO.EXE" command. For example: CBLOGO.EXE xxxxxx.bin myphoto.bmp
- 4. Flash the BIOS into mainboard by "AWDFLASH.EXE". For example: AWDFLASH xxxxxx.bin

Reboot the system,. You can see the new picture displayed on the screen. If you require more parameters information concerning "CDLOGO.EXE". Please you refer to it's on_line help. If you don't prefer the logo displayed on the screen during boot up, set the "Show Bootup Logo" option as Disabled in the "BIOS FEATURES SETUP" section of the BIOS.

* We reserve the right of modifying the default full-logo of QDI without further notification.

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RecoveryEasy

Introduction:

RecoveryEasy[™], the latest QDI innovation, is able to protect the system from being destroyed, by creating a so-called "mirror partition" for a current hard disk partition and backuping all the data to the mirror area. This ideal utility provides disk partition, disk data backup/recovery, CMOS settings backup/recovery and multi-boot functions. RecoveryEasy is also able to prevent the system from being attacked by different kinds of boot virus or other severe virus such as CIH. In case the system is ruined either by mistake or virus, the system can be recovered from the mirror partition. It applies the build-in BIOS technology that does not occupy either the hard disk space or the system memory. It's the best choice for both corporations and PC users.

Operation Process:

There are two hotkeys - Ctrl+Bksp and F12 for RecoveryEasy to enter "Partition" and "Recovery" user interfaces accordingly during BIOS booting up. If two or more hard disks are installed, use F5 key to choose the hard disk.

Partition Interface (see figure-1) 1.

Users can create and delete partitions/mirror partitions, activate partitions, and uninstall RecoveryEasy in Partition User Interface.

HD No:1 All Size:2000 M 04	M Size:188 M
Sys Partition	D E F
Mir Pertition	Е
A Partition C:599 Partition D:509 Partition D:209 Partition F:100 CENTE PAR DELET CREATE HIR DELET	M Usage: 25: x2 M Usage: 28: x2 M Usage: 28: x2 M Usage: 5: x2 R PAR ACTIVE FNR RE MIR UNINST SEM
	P12:to mirror-menu. ESC:exit
Crewte new partition	
	回联想

figure-1 Partition Interface

1.0 Install RecoveryEasy for the first time

a. The utility checks the previous disk partition at first, and displays the status of the first four partitions. If there are more than four disk partitions, users will be asked to delete the redundant disk partitions, since only four partitions that can be activated are allowed to exist. However, if there're only four or fewer partitions, users can follow the system prompt and choose to install RecoveryEasy based on the previous disk partitions. In this way, the original extension partitions will be changed to normal ones, and probably the sequence of the partitions will be changed also, but the contents contained in each partition will remain the same.

RecoveryEasy

- b. If choosing to install RecoveryEasy on an absolutely clear disk, the utility will delete all the previous partitions.
- c. The password is set as default setting "qdiqdi" after installing RecoveryEasy.

1.1 CREATE PAR

Function : Creates a new partition.

Limitation: When no disk space remains or 4 partitions already exist, this button is disabled.

Steps : After pressing the "CREATE PAR" button.

- a. The system will prompt whether users want to create a mirror partition for it or not.
- b. If answering "Y", input the new partition size in Megabyte. Notice that the maximum partition size that can be assigned is half of the left disk space, which is also displayed in the status line. Another half is for the mirror partition. If answering "N", the whole disk space left can be assigned. See figure-2.



figure-2 Create Partition

Note:

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- a. The system will prompt "Insert system floppy, then reset" when the first partition on the first hard disk is created.
- b. After using DOS6.xx boot disk to format C partition, the system should be reset in order to access the partition.
- c. In Windows system 1,048,576 bytes equal 1 Megabyte, while in RecoveryEasy 1,000,000 bytes equal 1 Megabyte, therefore a smaller size will be displayed in Windows system compared with the size displayed in RecoveryEasy.

1.2 DELETE PAR

Function : Deletes the last partition and its mirror partition.

Limitation: When no partition exists, this button is disabled.

Steps : After choosing this function, only the final partition can be deleted in order to keep the continuous disk space. If the warning message is confirmed, the partition will be deleted. By pressing "N" or "ESC" key, the system quits.

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1.3 ACTIVE PAR

Function : Implements multi-boot function by activating one of the partitions.

Limitation: When no partition exists, this button is disabled.

- : If there're two or more partitions, choose one of them by pressing F5 key. Steps
- Note : After setting active partition, a letter "A" will be shown in front of this partition.

1.4 CREATE MIR

Function : Adds mirror partition for the disk partition that has no mirror.

- Limitation : This function should be performed by order, for example, from partition
 - 1 to 4. If no disk space remains or the last partition has its mirror partition already, this button is disabled.
- Steps : After pressing the "CREATE MIR" button, use F5 key to choose the partition to create mirror. The partition of which the size is bigger than the left disk space will be ignored.

1.5 DELETE MIR

Function : Deletes the mirror partition.

Limitation: If there is no mirror partition, this button is disabled. This function should be performed in reverse order, for example, from partition 4 to 1.

Steps : After pressing the "DELETE MIR" button, only the final mirror partition can be deleted in order to keep the continuous disk space. If the warning message is confirmed, the mirror partition will be deleted. By pressing "N" or "ESC" key, the system quits.

1.6 UNINST SFW

Function : Uninstall RecoveryEasy.

Limitation: None.

- : After pressing the "UNINST SFW" button and the warning message is Steps confirmed, RecoveryEasy will be uninstalled. By answering "N", the system quits.
- Note : After RecoveryEasy is uninstalled, all the mirror areas have been disconnected with the relate partitions. If no partition is deleted or changed in size, or no other partition is created, users have chance to "Recover existing RecoveryEasy settings" when next time entering RecoveryEasy partition interface, meanwhile the password will be set as default setting "qdiqdi".

1.7 OTHERS

F12 : Switches to Recovery User Interface.

ESC : Exits from the Partition User Interface. If users made some mistakes, for example, wrongly delete a partition, do not press the "ESC" key, press the reset button on your system at once, in this way users can save their system.



RecoveryEasy

F5:

- a. When two or more than two hard disks are installed on the system, use F5 key to choose the hard disk. Every time users use F5 key to switch the hard disk, the operation result for the previous hard disk is saved. When processing a certain hard disk, F5 key can be used to choose the partition.
- b. In addition, when two or more than two hard disks are installed, the sign of partitions will be changed from C, D, E, F to 1, 2, 3, 4 accordingly.

2. Recovery Interface (see figure-3)

Users can backup the partition to its mirror area, and recover the partition from its mirror area from Recovery User Interface. This interface also provides users with CMOS settings backup or recovery, and changing password functions.



figure-3 Recovery User Interface

2.1 BACKUP PAR

Function : Backups the content of the partition to its mirror area. **Limitation** : If no mirror partition exists, this button is disabled. **Steps**:

- a. Use F5 key to choose the partition with mirror area existed.
- b. If the partition chosen has been backuped before, a warning message will be shown, and the time when last backup was done will be displayed in the status line. After confirming the warning message, the system performs the backup. By pressing "N" or "ESC" key, the system quits.

2.2 RE-CVR PAR

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Function : Recovers the content from the mirror area to the relate partition.

Limitation: If users didn't backup any partitions before, this button is disabled. Steps:

- a. Use F5 key to choose the backuped partition.
- b. The time when the latest backup was done will be displayed in the status line. After confirming the warning message, the system performs the content recovery. By pressing "N" or "ESC" key, the system quits.

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Note:

a. During the process of partition backup or recovery, a guage will be shown as below, the backup or recovery speed is about 4-5Mbyte/s. See figure-4.



figure-4 Backup Partition

b. If a disk I/O error occurs during the process of partition backup or recovery, this means there's physical damage on the hard disk, however users can ignore it and continue the process.

2.3 ATTRIB PAR

Function : Allows users to modify the properties of the partition (eg. FAT16 -> FAT32) after entering OS.

Limitation: None.

Steps : After pressing this button, turn on/off the switch.

Note:

- a. The switch resets to the default setting "disable" every time the system reboots.
- b. In order to implement this function, users need to enable the switch when installing the OS or modifying the partition properties. Please note: Do not create or delete partitions or change the partition size when modifying the partition properties.

2.4 BACKUP CMS

Function : Backups all CMOS settings.

Limitation: None.

Steps : After choosing this function, the current CMOS settings will be saved.

2.5 RE-CVR CMS

Function : Recovers all CMOS settings.

Limitation: None.

- **Steps** : After choosing this function, the latest backup of the CMOS settings will be recovered. The system needs reboot in order to validate the new CMOS settings.
- **Note** : If users have never backuped the CMOS settings, a wrong message will be shown after choosing this function.

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2.6 CHANGE PWD

Function : Changes the password to enter RecoveryEasy Partition or RecoveryUser Interface.

Limitation: None.

Steps : Follow the system prompt, input the password no more than 6 characters twice. To delete the password, follow the system prompt and press the "Enter" key twice.

Note:

- a. The password should be no more than 6 characters, only digital and alphabetic letters are valid.
- b. Once the password is enabled, users will be asked to input the password every time they try to enter the RecoveryEasy user interfaces, and up to 3 times try is permitted.

2.7 Others

Ctrl+Bksp : Switches to Partition User Interface.

- **ESC** : Exits from the Partition User Interface.
- **F5** : When two or more than two hard disks are installed on the system, use F5 key to choose the hard disk. When processing a certain hard disk, F5 key can be used to choose the partition.

FAQ:

1. What does RecoveryEasy do?

RecoveryEasy creates a so-called "mirror partition" with same size for the hard disk partition on the same hard disk, and then completely backups all the data sector by sector to the mirror area. This mirror partition is reserved to OS. When the OS ruins either by mistakes or virus, users can recover the partition from its mirror.

2. Does RecoveryEasy occupy the system resources?

Although some hard disk data protection applications can automatically protect the disk data in runtime, it lowers the system performance. Unlike these applications, RecoveryEasy need users to backup or restore datamanually when needed, but it DOES NOT lower the system performance when the system is running. It does not occupy either hard disk space or system memory, additional floppy disk or ISA/PCI cards are unnecessary.

3. RecoveryEasy utilizes Build-in BIOS skill, what is build-in BIOS?

RecoveryEasy build-in BIOS means all functions of RecoveryEasy including creating partition, backuping and restoring partition are built in BIOS. Users just need to down load the latest BIOS from our Website (http://www.qdigrp.com) when wanting to upgrade (It's free!).

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4. Are there any hard disk limitations of RecoveryEasy? RecoveryEasy supports all kinds of current IDE hard disks and has no limitation on

the hard disk capacity. RecoveryEasy can not provide its function for some special hard disk types such as SCSI, but it will not affect their usage.

5. Are there any OS limitations of RecoveryEasy? RecoveryEasy supports current operating systems such as DOS, Windows 95/98. However in Windows NT, Windows 2000, Unix and OS2 systems, users should notice that the disk tools bundled in the OS could change the mirror partition. On the other hand, since users can create partition with RecoveryEasy, it is unnecessary to use other disk tools.

6. Why does the system halt when HDD access mode is changed (eg. LBA->LARGE)?

This is a way to protect the system from the errors of data accessing caused by changing HDD access mode. When RecoveryEasy detects such things, the system will be locked, users could reboot the system and set the HDD access mode as the original one in BIOS SETUP.

7. Why does the remainder size plus partitions size not match the total size shown in RecoveryEasy sometimes? When the location of partitions is not continuous, the above problem exists.

8. Are there any other disk partition tools that can modify the partition table made by RecoveryEasy?

RecoveryEasy provides a write-protect function, so the disk tools such as Fdisk, Partition Magic, BootMenu, SmartDisk and BootStar can not modify the partition table created by RecoveryEasy. Some of the applications even terminate during operation. However the disk tools bundled in the OS such as Windows NT, Windows 2000, Unix and OS2 could change the mirror partition.

9. Why does it happen that a prompt "*installation can not continue*" pops up when installing Windows98 or a yellow exclamation mark shown beside IDE device in system properties?

During Windows 98 installation, the installation program will write to MBR (Master Boot Record) which is protected by RecoveryEasy, therefore the installation will be terminated. To avoid this problem, a "ATTRIB PAR" button is provided in Recovery User Interface. Enable this switch before installing Windows 98, then the installation will be successfully completed. In order to remove the yellow question mark before IDE devices in Device Manager, enable this switch once more after system reboot.

10. Why does the converting of FAT16->FAT32 in PQ Magic go wrong? MBR will be accessed when converting FAT16 to FAT32 with PQ Magic, which is protected by RecoveryEasy, therefore the conversion will be invalidate. Enabling the "ATTRIB PAR" switch from Recovery User Interface

RecoveryEasy

before converting can avoid this problem. It's the same situation as "FAT32 Converter" provided in Windows98.

11. What if partitions be wrongly deleted in RecoveryEasy?

If users delete a partition in RecoveryEasy by mistake, they can save it by pressing the Reset button on their system at once. Do not press the "ESC" key to quit RecoveryEasy, this will save the change. Do not try to create the partition again, since creating partition will clear all the content of the partition.

12. What is multi-boot?

RecoveryEasy can implement the multi-boot function by activating different partition. For example on the hard disk, partition C contains DOS, partition D contains Windows 95 version, partition E contains Windows 98 version, when activating partition C in RecoveryEasy, the system enters DOS, when activating partition E, the system enters Windows 98 version. At the same time, the sequence of the partitions is adjusted accordingly, partition E becomes C:, partition C becomes D: and partition D becomes E:. This function is the same as that of fdisk.exe, but the system needs reboot in order to make the change validate for fdisk.exe.

13. What if computer accidentally power off when backuping (recovering)? The partition should be completely backuped or recovered. If the computer accidentally powers off, the partition should be backuped or recovered once again.

14. What if users lose the password?

To make sure the security, the password is saved in the hard disk. It's very important for users to remember the password. If forgetting the password, contact us, clearing CMOS is useless.

15. Does RecoveryEasy protect hard disk against CIH?

RecoveryEasy can strongly protect the hard disk from boot-virus, as well as the attack of CIH. If the system is attacked by CIH, RecoveryEasy will automatically recover the MBR and each partition boot record before system boots up, and try to recover the FAT. In this way the system can basically boot up, then users can use some anti-virus application to kill the virus. However this depends on how CIH virus affects the system. CIH normally outbreaks on 26th every month, if the system cannot boot up that day, power off the computer instantly, and use the second safe way to recover the system, that is, recover the partition from its mirror area from Recovery User Interface. Remember to create a mirror partition and backup before virus attacks the system.

Item Checklist

Completely check your package. If you discover damaged or missing items, contact your retailer.

- Advance 10B/Advance 10F mainboard
- QDI Driver CD 2000
- I/O shield
- 1 IDE ribbon cable
- 1 floppy ribbon cable
- 1 10-pin ribbon cable with bracket for USB3 and USB4(manufacturing option)
- User's manual

Notice

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If you need any further information, please visit our web-site: "www.qdigrp.com".

Board Layout of Advance 10B/ Advance 10F Note:

Advance 10B V2.0 mainboard is that of the model with AC'97 Codec , Advance 10F V2.0 mainboard is that of the model without AC'97 Codec.

The AC'97 decsriptions in the manual is invalid for Advance 10F V2.0 mainboard.