The most important jumpers for Jamicon KM-T5-V rev. 2.0

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For directional descriptions to make sense, orient the processor socket to bottom side.

Conventions used in this document

Jumper orientations are always implied by the lines denoting 'open'.

JP4 -- Ext. Clock

This jumper block is on the left side, slightly above the CMOS battery. It is curiously oriented horizontally; pin 1 is to top right

	50 MHz	55 Mhz	60 MHz	67 MHz	75 MHz
21	**		**		
43	**	* *			
56					* *

JP13 -- Clock Multiplier

This jumper is immediately to the left of processor socket. Even more curiously, the jumpers go in vertical, and pin 1 is to top left.

		2.5x	3.0x	2.0x	1.5x
1	3	* *	*	*	1 1
2	4	* *	*	*	1 1

Please note that the above conventions for short and open will apply to the rest.

JP8 -- CMOS Data Discharge

	Maintain BIOS setup	Clear BIOS
	data in CMOS memory	setup data
1		*
2	*	*
3	*	

JP11, JP12, JP14, JP 15 and JP16 -- On-board Regulator Voltage

This is lots of jumpers. The first triplet is immediately to the right of the CMOS battery, with JP11 at top and JP14 at bottom. JP15 is to the left of CPU socket, JP 16 to the right of the socket.

JP11	JP12	JP14	JP15	JP16	
			13	13	Voltage
123	12345	123	24	24	
-	**	_	* *	* *	
			* *	* *	3.45V
**-	**	**-	* *	* *	
			* *	* *	3.30V
-**	*	-**	1.1		
			11	11	3.45V and 2.9V

					3.45V and 2.7V
_**	**	_**			3.45V and 2.5V
					3.30V and 2.9V
		**-			3.30V and 2.7V
_**		* * -			3.30V and 2.5V

usb1: power data1 data2 ground
usb2: ground data2 data1 power